

## Refine Search

### Search Results -

Term	Documents
(7 AND 13).PGPB,USPT.	7
(L13 AND L7).PGPB,USPT.	7

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L16

Refine Search

Recall Text



Clear

Interrupt

### Search History

 DATE: Monday, January 30, 2006    [Printable Copy](#)    [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
side by side			
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L16</u>	L13 and l7	7	<u>L16</u>
<u>L15</u>	L13 and l11	5	<u>L15</u>
<u>L14</u>	L13 and l3	22	<u>L14</u>
<u>L13</u>	(712/2-300)[CCLS]	11805	<u>L13</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L12</u>	L11 not l10	43	<u>L12</u>
<u>L11</u>	(redistribut\$5 or reallocat\$5 or reassign\$5) near5 (microinstruction\$1 or instruction\$1 or macroinstruction\$1 or cod\$3 or command\$3) near25 (plur\$7 or multip\$7 or dual or two or second) near5 (unit\$1 or cpu\$1 or alu\$1 element\$1 or pe\$1 or processor\$1)	47	<u>L11</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L10</u>	L9 and l8	5	<u>L10</u>

<u>L9</u>	(718/101-108)[CCLS]	4053	<u>L9</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L8</u>	L7 and (plur\$7 or multip\$7 or dual or two or second) near5 (unit\$1 or cpu\$1 or alu\$1 element\$1 or pe\$1 or processor\$1)	86	<u>L8</u>
	(redistribut\$5 or reallocat\$5 or reassign\$5) near5 (microinstruction\$1 or		
<u>L7</u>	instruction\$1 or macroinstruction\$1 or cod\$3 or command\$3) near8 (second or auxi\$6 or slave or co\$7 or less or low)	132	<u>L7</u>
<u>L6</u>	L5 and shar\$5	18	<u>L6</u>
<u>L5</u>	L3 near25 (plur\$7 or multip\$7 or dual or two or second) near5 (unit\$1 or cpu\$1 or alu\$1 element\$1 or pe\$1 or processor\$1)	37	<u>L5</u>
<u>L4</u>	L3 and (plur\$7 or multip\$7 or dual or two or second) near5 (unit\$1 or cpu\$1 or alu\$1 element\$1 or pe\$1 or processor\$1)	124	<u>L4</u>
<u>L3</u>	L2 near15 resourc\$3	195	<u>L3</u>
	(distribut\$5 or allocat\$5 or assign\$5 or design\$6) near5 (microinstruction\$1 or		
<u>L2</u>	instruction\$1 or macroinstruction\$1 or cod\$3 or command\$3) near8 (second or auxi\$6 or slave or co\$7 or less or low)	14386	<u>L2</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L1</u>	6658560.pn.	1	<u>L1</u>

END OF SEARCH HISTORY